

Form PTO-1449 (Modified)

LIST OF PATENTS AND PUBLICATIONS  
FOR APPLICANT'S  
INFORMATION DISCLOSURE STATEMENT  
(Use several sheets if necessary)

Atty Docket No.:  
YAMAP0705US

Serial No.: 09/539,206

Applicant: Fujiyama et al.

Filing Date: 03/30/00

Group:

## REFERENCE DESIGN AND U.S. PATENT DOCUMENTS

Examiner's Initials		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
BA	AA	5 0 0 0 0 3 0	11/16/99	Hein	360	45	
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						

## FOREIGN PATENT DOCUMENTS

Examiner's Initials		Document Number	Date	Country	Class	Subclass	Translation
							Yes No
	AI						
	AJ						
	AK						
	AL						
	AM						

## OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

BA	AN	B. Garlepp et al.; "A Portable Digital DLL Architecture for CMOS Interface Circuits"; 1988 Symposium on VLSI Circuits Digest of Technical Papers; pgs. 214-215.
	AO	
	AP	

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

## Information Disclosure Statement PTO-1449 (Modified)

The identification of any reference is not intended to be, and should not be understood as being, an admission that such publication, in fact, constitutes "prior art" within the meaning of applicable law since, for example, a given reference may have a later effective date than first seems apparent or the reference may have an effective date which can be antedated. The "prior art" status of any reference is a matter to be resolved during prosecution.